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Substitute for Form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete If Known

Application Number	10/647,931
Filing Date	8/26/2003
First Named Inventor	Trafton
Group Art Unit	ABE 2816
Examiner Name	TBD
Attorney Docket No.	TI-35750

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
JZ	AA	6,304,066		Wilcox et al.	10-16-2001	
JZ	AB	6,288,524		Tsujimoto	09-11-2001	
	AC					
	AD					
	AE					
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FOREIGN PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Office ³	Number ⁴	Kind Code ² (if known)				
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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Jeffrey Zweizig 11/17/04

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Art Unit	2816
Examiner Name	
Attorney Docket Number	TI-35750

Sheet 1

of

1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JZ		ON Semiconductor CS5111 Data Sheet, Rev. 9 (Semiconductor Components Industries, LLC, January 2001).	
JZ		"Linear Regulators: Theory of Operation and Compensation", App.Nt. 1148 (Nat'l Semiconductor, 2000).	
JZ		"Linear and Switching Voltage Regulator Fundamentals" (National Semiconductor).	
JZ		"DC/DC Conversion without Inductors", APP 725 (Dallas Semiconductor, 2000).	

Examiner Signature	Jeffrey Zweig	Date Considered	11/17/04
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